

DOT1	WS	REP 1	DOT WS	REP 2	DOT WS	REP 3	DOT WS	REP 4	DOT WS	REP 5	DOT WS	REP 6	DOT
101	11	40	37	11	40	37	11	40	37	11	40	37	37
bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit

WS	REP 7	DOT WS	REP 8	DOT WS	REP 9	DOT WS	REP 10	DOT WS	REP 11
11	40	37	11	40	37	11	40	37	11
bit	bit	bit	bit	bit	bit	bit	bit	bit	bit

FIG. 1

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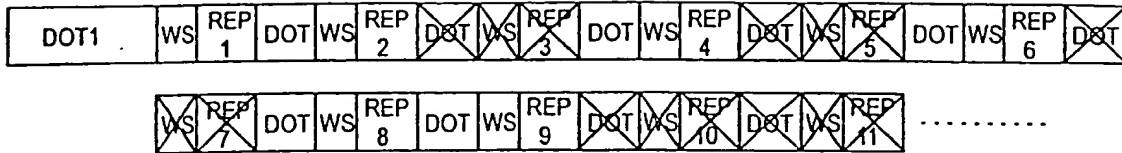


FIG. 2a

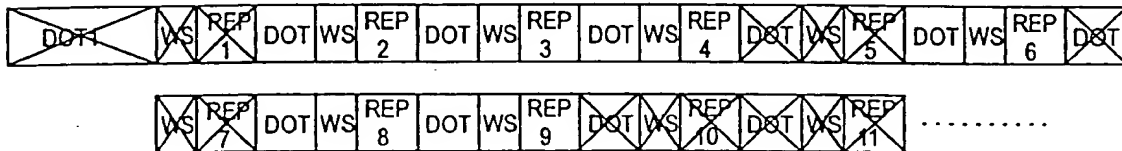


FIG. 2b

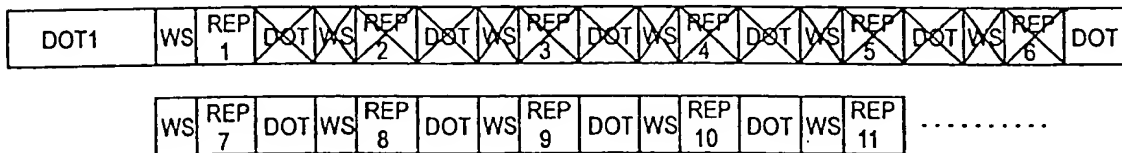


FIG. 2c

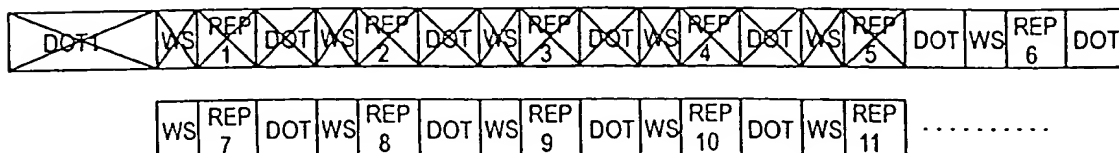


FIG. 2d

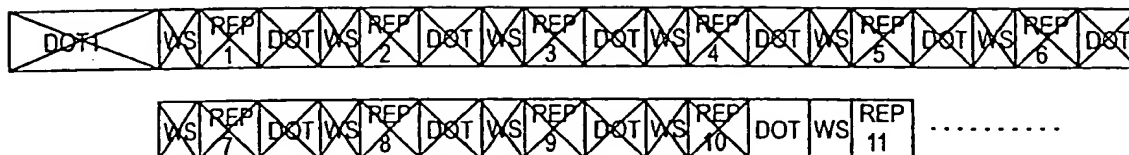


FIG. 3a

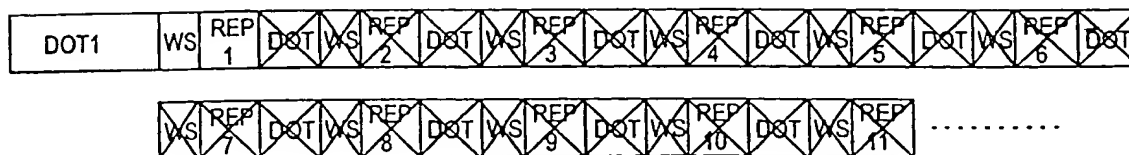


FIG. 3b

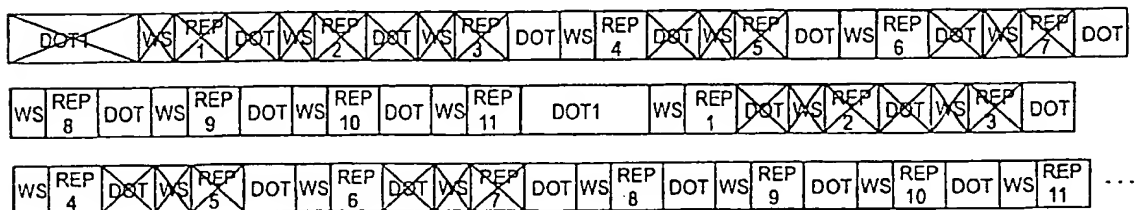


FIG. 4a

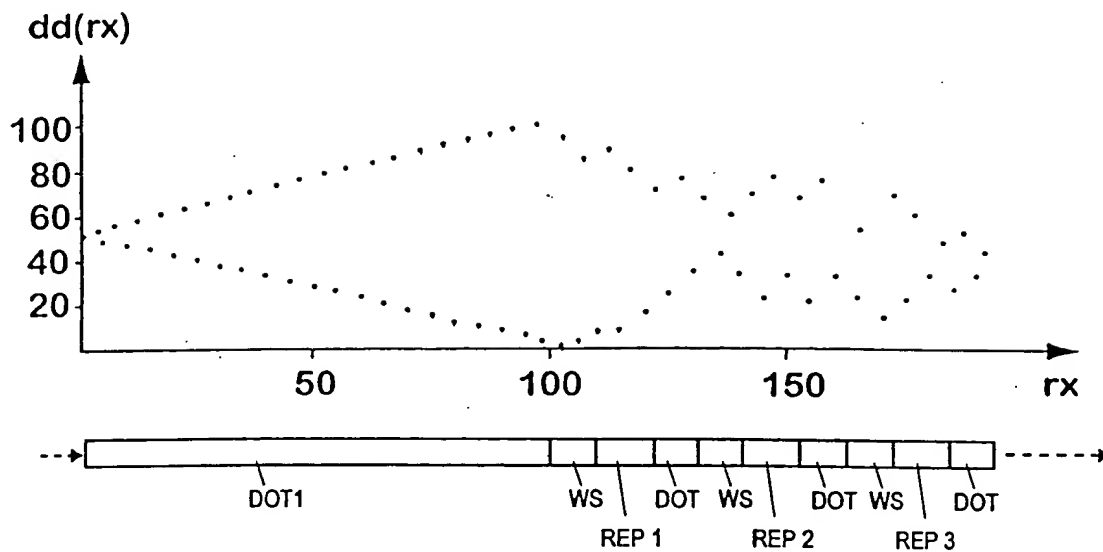


FIG. 4b

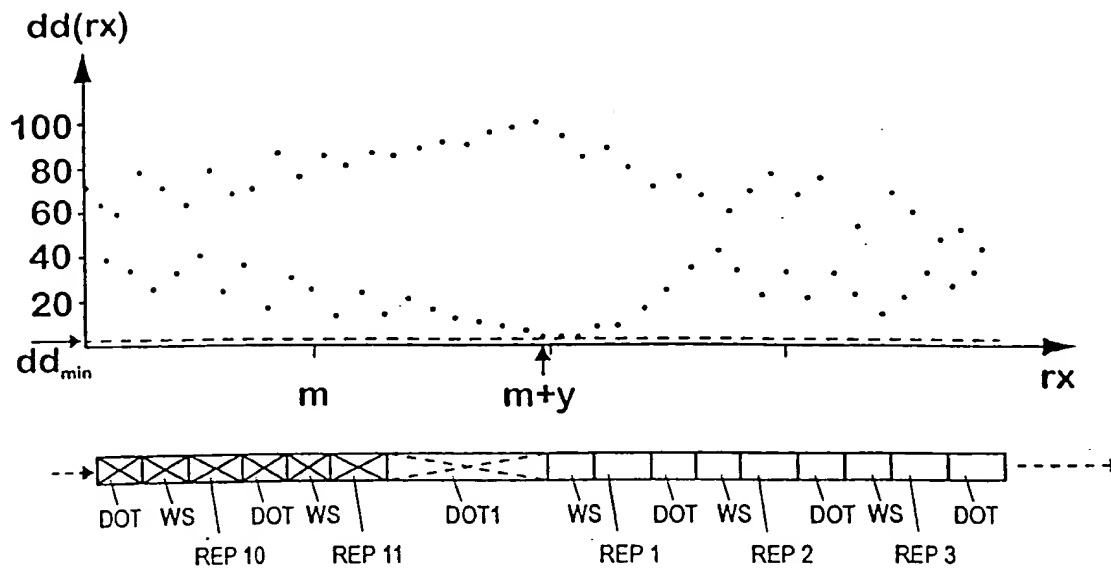


FIG. 4c

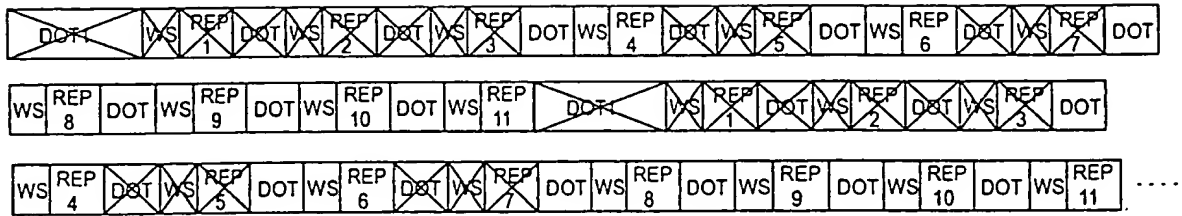


FIG. 4d

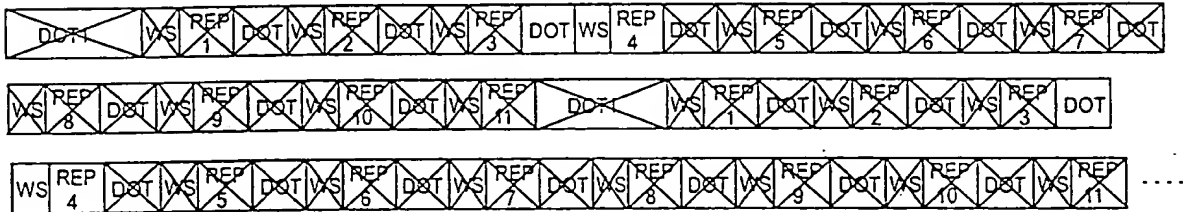


FIG. 5